

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Shunpei YAMAZAKI)
Application No. 09/255,777)
Filed: February 23, 1999) Art Unit: 2674
For: SEMICONDUCTOR DEVICE AND) Confirmation: 9041
METHOD FOR FORMING THE SAME)



SUPPLEMENT TO PRIOR INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

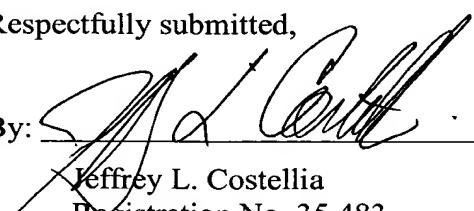
In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby clarify the Information Disclosure Statement filed on October 26, 2004.

Specifically, it should be noted that only JP 02-121369; JP 59-117164; and JP 58-016566 were cited in a counterpart Japanese Patent Application in an Office Action mailed on September 7, 2004. The remaining references cited independent of this foreign Office Action. As a result, consideration of each of the references cited in the October 26, 2004 Information Disclosure Statement is hereby requested in view of the filing of an RCE filed simultaneously herewith.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380 (740756-1936).

Respectfully submitted,

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